

IN THE CLAIMS:

1. (Currently Amended) A broadcast data receiver apparatus for receiving at least one signal carrying video data, said signal transmitted from a video data encoder, said signal having a voltage value within a known voltage range and said signal required to pass through at least one amplifier in the processing of the same: said apparatus comprising:

a video data amplifier and driver circuit ~~which in operation is adaptable~~ that adapts in response to changes in the supply voltage and environmental conditions in which said broadcast data receiver operates;

said circuit including a single input transistor and two further transistors used in forming an amplifier;

said circuit ~~and compensates~~ compensating for said changes in the supply voltage and environmental conditions via generation of a level of DC offset on ~~an~~ said single input transistor; and ~~of the video data amplifier and driver circuit;~~

said DC offset value added to the video data signal to form a combined signal.

2. (Previously Presented) An apparatus according to claim 1 wherein said environmental change relates to a change in temperature.

3. (Previously Presented) An apparatus according to claim 2 wherein said change in environmental condition includes the change in level of amplification of a bias voltage.

4. (Previously Presented) An apparatus according to claim 1 wherein the compensation in the video data amplifier and driver circuit provides said DC offset at the input transistor of a calculated compensatory value.

5. (Previously Presented) An apparatus according to claim 1 wherein said known voltage range of the video signal is 0V to 5V.

6. (Previously Presented) An apparatus according to claim 5 wherein said known voltage range of the video signal is 0 to 1V.

7. (Currently Amended) A method of processing a received video data signal by a broadcast data receiver, said method comprising the steps of:

receiving the video signal having a voltage value within a known voltage range and being required to pass through at least one amplifier in the processing of the same;

passing the video signal through a video data amplifier and driver circuit including a single input transistor and two further transistors used in forming an amplifier which adapts the video signal as it passes therethrough with reference to the supply voltage and environmental conditions in which the broadcast data receiver operates;

said circuit generating via said single input transistor at least one compensatory value which alters with respect to changes in the supply voltage and environmental conditions; and

said at least one compensatory value being added to the video signal as the video signal passes through ~~the~~ said circuit to form a combined signal.

8. (Previously Presented) A method according to claim 7 wherein said compensatory value is a level of DC offset added to the video data signal to form said combined signal.
9. (Previously Presented) A method according to claim 7 wherein a video data signal multiplication factor is generated in the circuit and used to multiply said combined signal as it passes through the circuit.
10. (Previously Presented) An apparatus according to claim 1 wherein said change in environmental condition includes the change in level of amplification of a bias voltage.
11. (Currently Amended) A broadcast data receiver apparatus, said apparatus comprising:
- receiving means for receiving at least one signal carrying video data and being transmitted from a video data encoder and having a voltage value within a known voltage range;
 - at least one amplifier wherein said signal is passed through during processing;
 - a video data amplifier and driver circuit having ~~an~~ a single input transistor and two further transistors used in forming an amplifier
- said circuit ~~and~~ generating a level of DC offset ~~on the~~ said input transistor in response to changes in the supply voltage and environmental conditions in which the broadcast data receiver operates and ~~compensates~~ compensating for said changes in the supply voltage and environmental conditions; and
- said circuit generating a combined signal which includes said DC offset value added to the video data signal.

12 (Previously Presented) Apparatus according to claim 11 wherein said environmental change relates to a change in temperature.

13. (Previously Presented) Apparatus according to claim 11 wherein said change in environmental condition includes the change in level of amplification of a bias voltage.

14. (Currently Amended) Apparatus according to claim ~~13~~ 11 wherein the compensation in the video data amplifier and driver circuit provides said DC offset at the input transistor of a calculated compensatory valve. ~~said change in environmental condition includes the change in level of amplification of a bias voltage.~~

15 (Previously Presented) Apparatus according to claim 11 wherein said known voltage range of said video signal is 0V to 5V.

16. (Previously Presented) Apparatus according to claim 11 wherein said known voltage range of the video is 0 to 1V.